

匠芯创M76H/75H,76P/73P,76/73,76E/73E系列工业级高性能实时处理器DSP选型表

PN	Core	Cordic	HCL	FFT	PFM	DCE	GPT	Memory			Signal Chain			Control device							PBUS	Interface							GPIO	Package	Operating Temperature Range(Tj)	Package qty		
								PSRAM	SRAM	FLASH	ADC	PGA	DAC	HRPWM /PWM	CAP	QEP	QOUT	CPM	SDF M	TA-IF/BIS-IF /EDAT-IF		ESC	PHY	USB	EMAC	UART	QSPI	SPI					I <sup>2</sup> C	CAN
<b>M76H00/M75H00</b>																																		
M76H06SGHS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/18CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	Y	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	87	BGA144 10x10mm,0.8mm	-40~125°C	盘240 包2400
M75H06SGHS	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	Y	Y	Y	x4	N	768KB	2MB	14bit 2Msps x2/18CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	Y	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	87	BGA144 10x10mm,0.8mm	-40~125°C	盘240 包2400
<b>M76P00</b>																																		
M76P09SGKS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	4MB	1MB	4MB	14bit 2Msps x2/24CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	137	BGA196 12x12mm,0.8mm	-40~125°C	盘168 包1680
M76P08SGKS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/24CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	150	BGA196 12x12mm,0.8mm	-40~125°C	盘168 包1680
M76P06SPHS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	122	eLQFP144 20x20mm,0.5mm	-40~125°C	盘60 包600
M76P06SGHS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	118	BGA144 10x10mm,0.8mm	-40~125°C	盘240 包2400
<b>M73P00</b>																																		
M73P06SPHS	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	122	eLQFP144 20x20mm,0.5mm	-40~125°C	盘60 包600
M73P06SGHS	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	118	BGA144 10x10mm,0.8mm	-40~125°C	盘240 包2400
<b>M7600</b>																																		
M7609SGKS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	4MB	1MB	4MB	14bit 2Msps x2/24CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	137	BGA196 12x12mm,0.8mm	-40~125°C	盘168 包1680
M7608SGKS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/24CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	150	BGA196 12x12mm,0.8mm	-40~125°C	盘168 包1680
M7606SPHS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	122	eLQFP144 20x20mm,0.5mm	-40~125°C	盘60 包600
M7606SGHS	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	118	BGA144 10x10mm,0.8mm	-40~125°C	盘240 包2400
M7605SPES	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x2	80	eLQFP100 14x14mm,0.5mm	-40~125°C	盘90 包900
M7605SNES	2x(RISC-V 552MHz +FPU+DSP)	x1	x2	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/12CH	x4	N	16CH/16CH	x6	x3	x1	x2	x4	x1/x1/x1	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	85	QFN100 12x12mm,0.4mm	-40~125°C	盘189 包1890
<b>M7300</b>																																		
M7305SPES	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x2	80	eLQFP100 14x14mm,0.5mm	-40~125°C	盘90 包900
M7305SNES	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/12CH	x4	N	16CH/16CH	x6	x3	x1	x2	x4	x1/x1/x1	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	85	QFN100 12x12mm,0.4mm	-40~125°C	盘189 包1890
M7303SPCS	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/13CH	x4	N	9CH/9CH	x6	x6	x1	x2	x4	x1/x1/x1	N	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x1	47	eLQFP64 10x10mm,0.5mm	-40~125°C	盘160 包1600
M7303SNCS	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/15CH	x4	N	12CH/12CH	x6	x7	x1	x2	x4	x1/x1/x1	N	N	N	x1	N	x8	x2	x5	x4	CAN x1 CAN-FD x2	55	QFN68 7x7mm,0.35mm	-40~125°C	盘260 包2600
<b>M76E00</b>																																		
M76E08SGKS	2x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/24CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	150	BGA196 12x12mm,0.8mm	-40~125°C	盘168 包1680
M76E06SPHS	2x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	122	eLQFP144 20x20mm,0.5mm	-40~125°C	盘60 包600
M76E06SGHS	2x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	118	BGA144 10x10mm,0.8mm	-40~125°C	盘240 包2400
M76E05SPES	2x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x2	80	eLQFP100 14x14mm,0.5mm	-40~125°C	盘90 包900
M76E05SNES	2x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	1MB	2MB	14bit 2Msps x2/12CH	x4	N	16CH/16CH	x6	x3	N	x2	x4	N	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	85	QFN100 12x12mm,0.4mm	-40~125°C	盘189 包1890
<b>M73E00</b>																																		
M73E05SPES	1x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x2	80	eLQFP100 14x14mm,0.5mm	-40~125°C	盘90 包900
M73E05SNES	1x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/12CH	x4	N	16CH/16CH	x6	x3	N	x2	x4	N	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	85	QFN100 12x12mm,0.4mm	-40~125°C	盘189 包1890
M73E03SPCS	1x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/13CH	x4	N	9CH/9CH	x6	x6	N	x2	x4	N	N	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x1	47	eLQFP64 10x10mm,0.5mm	-40~125°C	盘160 包1600
M73E03SNCS	1x(RISC-V 552MHz +FPU+DSP)	x1	N	Y	Y	Y	x4	N	512KB	1MB	14bit 2Msps x2/15CH	x4	N	12CH/12CH	x6	x7	N	x2	x4	N	N	N	N	x1	N	x8	x2	x5	x4	CAN x1 CAN-FD x2	55	QFN68 7x7mm,0.35mm	-40~125°C	盘260 包2600
<b>M6800</b>																																		
M6806SPES	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	N	N	N	N	N	1MB	2MB	12bit 4Msps x2/16CH	N	N	6CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	1	1	8	2	3	3	CAN x2	84	LQFP100 14x14mm,0.5mm	-40~125°C	盘90 包900
M6806SNES	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	N	N	N	N	N	1MB	2MB	12bit 4Msps x2/16CH	N	N	6CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	1	1	8	2	3	3	CAN x2	88	QFN100 12x12mm,0.4mm	-40~125°C	盘189 包1890
M6801SPCS	1x(RISC-V 552MHz +FPU+DSP)	x1	x1	N	N	N	N	N	512KB	1MB	12bit 4Msps x2/15CH	N	N	6CH/12CH	x3	x5	x1	x2	x2	x1/x1/x1	N	N	N	0	0	5	1	1	2	CAN x1	49	LQFP64 10x10mm,0.5mm	-40~125°C	盘160 包1600